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A comprehensive defect study of tungsten disulfide (WS₂) as electron transport layer in perovskite solar cells by numerical simulation

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ABSTRACT

In this study, an ideal planar perovskite solar cell (PSC) has been proposed and simulated by using Tungsten Disulfide (WS₂) as an electron transport layer (ETL). Effects of various amphoteric defect states of PSC based on $CH_3NH_3PbI_{3-x}X_x$ absorber layer and the interface properties of both ETL and hole transport layer (HTL) are quantitatively analysed by SCAPS-1D numerical simulator. Results show that the device performance is highly influenced by amphoteric defects in the absorber layer rather than the interface defects layer (IDL). It is also revealed that the quantitative tolerable range in $CH_3NH_3PbI_{3-x}X_x$ and IDLs are less than 10^{15} cm⁻³ and 10^{16} cm⁻³, respectively. The PSC exhibits better performance in the range of 10 °C–40 °C and degrades gradually at higher temperature. With the proposed structure, the simulation finds the highest power conversion efficiency (PCE) of PSC to be 25.70% (Voc = 1.056 V, Jsc = 25.483 mA/cm², and FF = 88.54%).

Introduction

The hybrid organic–inorganic perovskite solar cells (PSCs) based on methylammonium lead halide $CH_3NH_3PbX_3$ (X is Cl, Br or I) have acquired a great interest in research community due to high-power-conversion efficiency (PCEs) [1–3]. The underlying PCE of around 10% has quickly been supplanted by higher values ranging from 12% to more than 22% within a brief timeframe [4–13]. Due to continuous advancement of device architectures and material processing technologies, fabrication of PSC stands out amongst the most bracing new photovoltaic technology today. This noteworthy increment in execution, which has emphatically empowered numerous exploratory and hypothetical examinations, brings the end goal to upgrade the outline of the structure and the choice of materials. Several works are aimed for focusing on the innovative execution that guarantees to accomplish high PCEs at lower cost and flexibility [1,14,15].

In assessment to the fast development in device efficiency, the basic understanding regarding the defect properties that play a major role in general device performance remains restrained. It is delicate to say that a semiconductor such as perovskite having doped by its own crystalline will encounter defects [16,17]. In particular, defects inside the band gap with deep transition can entice carriers, which results into Shock-ley–Read–Hall non-radiative recombination centres. Thus, the investigation of the defect properties in halide perovskite photovoltaics is needs to steer for further improvement of the potency. It is already reported that the defect energy distribution in the CH₃NH₃PbI₃ layer in PSC has a deep defect state of 0.16 eV above the valence band. As per hypothetical calculations, the defect state is potentially ascribed to solely low-energy deep traps [18]. It is also revealed that these defects in nature [19].

In PSC, a few hundred nanometre-thick perovskite absorber layer is sandwiched between an electron transport layer (ETL) and hole transport layer (HTL). This absorber layer can be with or without mesoporous scaffold. Carriers like electrons and holes, are created after the absorption of light. Thus, being excited by incident photon, carriers

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travel through a hauling passage. Thereby it is needless to mention that this carrier pathway, from the absorber to both electrodes, have a great impact on PCE [20,22,23]. In recent times, the incorporation of new materials in the above-mentioned pathway, especially in ETL, has drawn huge attention of the scientific community to further improve the PCE of PSC [24-30]. TiO₂ is a notable inorganic material that is commonly used as the ETL in PSCs [20]. It has an electron mobility of approximately $1 \text{ cm}^2 \text{V}^{-1} \text{s}^{-1}$ with conduction band minimum (CBM) of 4.2 eV [21]. The CBM of TiO₂ is moderately 0.3 eV lower than MAPbI₃ [22,23]. Due to this, it facilitates the infusion of photo-generated electrons from perovskite absorber to the ETL. It also possesses low interfacial recombination with slightly lower charge extraction attributes. However, it has moderately low electron mobility and TiO₂based PSCs degrade quickly once exposed to ultraviolet radiation illumination [22-24]. Furthermore, TiO₂ commonly requires high-temperature sintering of around 500 °C, which expands the vitality payback time and limits the production of flexible solar cells [1].

Despite of the fact that the most efficient PSCs to date are having TiO₂ and ZnO based materials [25-27], some metal oxides such as SnO₂, Nb₂O₅, In₂O₃ and SrTiO₃ have proven potential to be photoelectrodes in dye-sensitized solar cells (DSSCs). However, they are found to be inferior in overall performance in comparison to the TiO₂ in DSSCs. This drawback widens the scope of developing new materials in respect to the understanding of PSC's performance. Recently, transition metal dichalcogenides (TMDCs), such as molybdenum disulphide (MoS₂), tungsten disulphide (WS₂) have gained significant attention in the PV community due to their unique electrical, optical and electrochemical properties, which make them promising materials in visiblelight optical devices [28,29]. These TMDCs have been applied to Copper indium gallium (di)selenide (CIGS) or Cadmium telluride (CdTe) solar cells and achieved noteworthy success [30-32]. Among the TMDCs, WS₂ exhibits high carrier mobility, good conductivity in the order of $10^{-3} \Omega^{-1} \text{ cm}^{-1}$ and native n-type semiconducting characteristics [34,41]. Li et al. reported that WS₂ exhibits excellent electron conduction property in dye synthesized solar cell. Moreover, it can be deposited through a solution process or by sputtering at low temperature [32,33-35]. In the recent past some researchers have successfully fabricated TMDC in organic solar cell devices, including bulk-heterojunction, organic-inorganic quantum dot solar cells and perovskite solar cells. It is already reported that layered MoS2 has been used as the hole-transport layer (HTL), electron-transport layer (ETL), interfacial layer, and protective layer in fabricating heterojunction solar cells. [56-58] Therefore, a highly stable, non-toxic and environmentally friendly n-type semiconductor makes WS₂ a potential candidate as the ETL in PSC.

In this study, we have proposed to evaluate WS₂ as a possible alternative for TiO₂ in planar perovskite solar cells. The key parameters of the PSC with the configuration of FTO/WS₂/CH₃NH₃PbI_{3-x}Cl_x/2,2',7,7'-tetrakis (N, N'-di-p-methoxyphenyl amine)-9,9-spirobifluorene (Spiro-OMeTAD)/Ni have been optimized by numerical computation using SCAPS-1D simulation programme. Amphoteric defects in CH₃NH₃PbI_{3-x}Cl_x absorber layer and both of its interface properties (WS₂/CH₃NH₃PbI_{3-x}Cl_x and CH₃NH₃PbI_{3-x}Cl_x/Spiro-OMeTAD) are thoroughly investigated in order to understand their effects on the PSC's performance.

Simulation setup and parameter

SCAPS-1D program (version 3.3.05) [38] has been commonly used for numerical simulations of a solar cell devices [39,40]. Many previous studies [33,35,41,45] have utilized SCAPS software to display planar structure of PSC (p-i-n) because of their structural similarity with other types of solar cells. This simulation program numerically solves three essential semiconductor equations: The Poisson equation and the continuity equation for holes and the continuity equation for electrons. In this study, an escalated hypothetical numerical investigation has been



Fig. 1. A schematic structure of a Perovskite Solar Cell.

performed to outline ideal ETL instead of TiO_2 and to explore solar cell device mechanism for potency and device stability in ambient conditions. The examined device structure of this study is shown in Fig. 1.

In this work, intrinsic perovskite is used as an absorber layer with the proposition of n-i-p planar PSC structure. This model has been simulated with basic three input layers; p-type Spiro-OMeTAD is used as HTL, intrinsic perovskite (CH3NH3PbI_{3-x}Clx) as absorber layer and ntype WS₂ as ETL. Hence solar energy is considered to enter through the ETL layer. A standard AM1.5G spectrum (1000 W/m²; T = 300 K) has been used for illumination. Typical thicknesses of each layer were fixed. In particular, 350 nm thick perovskite absorber layer is considered as it ensures a radiation absorption close to the maximum, without major recombination losses [36-38]. For initial consideration, ETL and HTL thickness are set to be 100 nm and 150 nm respectively [41,46,47]. Interface defect layers (IDL) of 10 nm thickness were used in the perovskite/ETL interface to determine the effect of defect densities that subsist at material interfaces. Moreover, to replicate the defects of PSC, amphoteric native defect model is used in simulation where the density of defect in active layer was varied. Fig. 2 demonstrates the band alignment of the proposed model and Table 1 summarizes the physical parameters used for each layer in the numerical analysis. All the



Fig. 2. Band alignment of proposed PSC structure.

Table 1Parameters used for simulation study.

Parameters	CH ₃ NH ₃ PbI ₃	IDL (Perovskite /ETL interface)	WS_2	Spiro-OMeTAD
Thickness (nm)	350	10	100	150
E_g (eV)	1.55	1.55	1.8	3.06
χ (eV)	3.93	3.9	3.95	2.05
£r	18	18	13.6	3
μ_e (cm ² /Vs)	3	3	100	$2.0 imes10^{-4}$
μ_h (cm ² /Vs)	17	17	100	$2.0 imes10^{-4}$
$N_D ({\rm cm}^{-3})$	$2.0 imes10^{10}$	$2.0 imes10^{10}$	$1.0 imes10^{18}$	-
$N_A ({\rm cm}^{-3})$	$2.0 imes10^{10}$	-	-	$1.0 imes 10^{18}$
$N_t ({\rm cm}^{-3})$	1.0×10^{10}	$1.0 imes 10^{15}$	1.0×10^{15}	$1.0 imes 10^{15}$

parameters used in this study are supported by various literature [35,39-44,46,47].

Despite of the parameters depicted in the Table 1, following values are considered irrespective of type of layers: thermal velocity of electrons and holes of 1×10^7 cm/s; Gaussian defect distribution all defect states, ETL and HTL interfaces defects are at the middle of interface gap and characteristic energy of 0.1 eV.

Results and discussion

Baseline WS₂/CH₃NH₃PbX₃/Spiro-OMeTAD solar cell devices

Initial simulation has been conducted using device geometry as cathode/WS₂/CH₃NH₃PbX₃/Spiro-OMeTAD/ Ni depicted in Fig. 1. As there is no reported experimental work on WS₂ as buffer layer, CH₃NH₃PbX₃ based device in the n-i-p configuration with TiO₂ and Spiro-OMeTAD as

ETL and HTL respectively; has been taken into consideration for comparison between simulated J-V and QE curves shown in Fig. 3(a) and (b) with experimental results [48]. The discrepancy in Jsc is likely because of different buffer layer material and non-consideration of refractive indexes of layers by SCAPS 1D. SCAPS 1D underestimates QE within the longer wavelength spectral region where light moves for the second pass to complete absorption, and so, a higher limit of optical Jsc is conferred. Dielectric function spectra of all the layers are also considered by optical simulation and thus, multiple reflections at various interfaces play a big role in QE. As comparison is done between two different materials, there exists differences between their refractive and reflective indexes which leads to the result of different QE of studied device. Most importantly, the proposed FTO/WS₂/ CH₃NH₃PbI₃–_xCl_x/ Spiro-OMeTAD/Ni device shows the efficiency of 25.70% with Jsc of 25.48 mA/cm², fill factor of 88.84% and Voc of 1.05 V in SCAPS-1D.

Being a baseline simulation study, no defect is considered in this simulation.

Effects of the amphoteric defect state in MAPbX₃

In perovskite $CH_3NH_3Pb-X_3$ (X = Cl, Br, I) layer, CH_3NH_3 and Pb provide one and two electrons, respectively, to three halide (X) particles, forming a bandgap of 1.5 eV. In forming Pb-X₂, the CH₃NH₃ molecule does not have any significant commitment but to donate an electron. Interestingly, CH₃NH₃Pb-X₃ layer exhibits different types of conductivity, intrinsic good p-type, moderate n-type, based on deposition properties of Pb and halide molecules. Such flexible characteristic of CH₃NH₃Pb-X₃ lead to defect such as their native defect or other various interface defects [49-51]. Defect properties in absorber layer of solar cell play a critical role to determine the electron-hole diffusion length and open circuit voltage, (Voc). Deep level defects usually act as Shockley-Read-Hall non-radiative recombination centres. It is proved that these types of defects are responsible for short minority carrier lifetime which results into lower Voc in solar cell. Again, if the defect state reaches up to the transition level for instance, at Fermi level position, then this can also act as Shockley-Read-Hall nonradiative recombination centres by donating/accepting electrons in the bandgap. Initially the idea of amphoteric defects was acquainted with clarifying impacts of native (or inherent) defects on the properties of semiconducting materials including the sticking of the Fermi level energy on semiconductor surfaces, stabilization of the Fermi energy in vigorously defective semiconductors arrangement of Schottky boundaries and drawbacks of doping of semiconductors. More recently the concept of amphoteric native defects has been used to control defect incorporation in compound semiconductors like CH₃NH₃Pb-X₃ in PSC devices [52].

Fig. 4 shows the effects of amphoteric defect states to MAPbI₃. Both the efficiency and Voc varies with the density of defect states in MAPbI₃. From the Fig. 4(a), it can be seen that when the density of defect states in MAPbI₃ increases from 13th order to higher value, the Voc reduces significantly and causes drop of efficiency (less than 10%). In addition, when the density of defect states is lower than 1×10^{15} cm^{-3} , the Voc remains above about 0.8 V and efficiency shows above 15% on that range. Moreover, if the density of the defect state is larger than 1×10^{15} cm⁻³, the Voc decreases to about 0.5 V which is impractical for solar cell application. Fig. 4(b) depicts the change of Jsc versus the density of defects in MAPbI₃ with respect to the efficiency of a cell. The figure shows a clear impact of amphoteric defect on Jsc and efficiency. Jsc changes from 25.4 to 20.8 mA/cm² with the change of density of the defect state from 1×10^{10} to 1×10^{13} cm⁻³. In this range, cell efficiency drops very little, only by 4.6%. Efficiency drops significantly when density of the defect state is higher than



Fig. 3. (a) J-V curves and (b) QE curves obtained from SCAPS 1D simulation as compared with an experimental data [48] for a n-i-p perovskite device.



Fig. 4. Effects of the change of (a) Efficiency and Voc versus the amphoteric defect density Nt and (b) Jsc and efficiency versus the amphoteric defect density in CH₃NH₃Pb-X₃ (MAPb-X₃).

 $1\times 10^{14}\,\rm cm^{-3},$ the Jsc and efficiency falls sharply with the increment of amphoteric defect state from $1\times 10^{16}\,\rm cm^{-3}.$

Effects of defect states in IDL2

Effects of defect states in IDL1

Effects of defect states in both the interfaces of PSC, IDL1 (WS₂/MAPbI₃) and IDL2 (MPbI₃/Spiro-OMeTAD), have been studied in detail. Figs. 5 and 6 represent the defect study in IDL1 and IDL2, respectively. It clearly shows that the cell efficiency drops drastically when defect density of IDL1 is 1×10^{18} cm⁻³ or more. Moreover, it is revealed that there is slight change of efficiency with the range of IDL1 defect state $1 \times 10^{15} \sim 1 \times 10^{17}$ cm⁻³. But in case of Voc, it has been observed that the Voc drops from 0.85 V to 0.68 V when defect density increases from 1×10^{16} cm⁻³ or higher values. There is hardly any change of Voc beyond 1×10^{16} cm⁻³ of defect density in IDL1.

Almost the same results depict for IDL2 as shown in Fig. 6. It has been examined that the cell efficiency goes significantly downward, 22% to 12%, when defect density at IDL2 is increased from 1×10^{17} cm⁻³ or more. In the range of defect state density 1×10^{14} – 1×10^{16} cm⁻³ in IDL2, device efficiency changes a little (25.4%–21.31%). In the case of Voc, it has been observed that Voc drops from 1.1 V to 0.697 V when defect density is 1×10^{15} cm⁻³ or more. It is also found (Fig. 6b) that defect plays a cogent role when it is near to the MPbI3/Spiro-OMeTAD interface, 0.1 eV–0.4 eV, Voc and efficiency are also found to be lesser in this condition. But if the defect state is far from the interface (above 0.9 eV); it has a very limited impact both in Voc and efficiency of the PSC. It is examined that fill factor of PSC is highly affected by the increment of defect; when the density of the defect state in IDL2 achieves the value of 1×10^{16} cm⁻³ or more, the FF decreases (Fig. 6C) from 87% to around 81%. The simulation also



Fig. 5. Effect of the change of (a) Efficiency with defect density and (b) Voc with respect to defect position versus various defect density in IDL1 of MAPDI3.



Fig. 6. Effect of the change of (a) Efficiency with the defect density and (b) Voc with respect to defect position versus various defect density (c) fill factor and (d) Jsc with the density of defect in IDL2 of MAPbI_{3.}

shows that density of defect has very less impact on Jsc (Fig. 6d).

Effects of temperature on PSC

Solar cells are generally installed in the outdoor environment. Thus, sunlight falls on it directly and causes increment in temperature, even by 45 °C higher than environmental temperature. For example, if the environmental temperature is 30 °C, the device temperature can rise up to 75 °C [54]. From various experiment on PSC, it showed that the most challenging concern for PSC is long-term stability [53,55]. The main reason for degradation is decrement in the shunt resistance which results lowering of both Voc and Fill Factor [54]. Researchers found that in high temperature and humid atmosphere, $CH_3NH_3PbI_3$ could be corroded by 4-*tert*-butylpyridine (4-tBP) and may also change the phase, which causes degradation of PSC performance [53].

Fig. 7 shows the effect of working temperature on the performance of PSC solar cell by changing environmental temperature from 10 °C to 90 °C. It is found (Fig. 7a) that both Voc and FF are badly affected by the higher temperature, which confirms to experimental study done by Han et al. [55]. But there are hardly any changes that can be observed in Jsc. As higher temperature affects both Voc and FF, the efficiency eventually reduces. As shown in Fig. 7b, this study confirms that PSC at ambient atmosphere (10 °C to 40 °C) provides higher efficiency, which is above 23%; but gradually decreases as the temperature rises. This finding can play a crucial role in selecting PSC in tropical regions.

Conclusion

In an attempt to enhance device stability, improve performance and

curtail hysteresis behaviour in PSCs, the possibility of WS2 as an alternative ETL material has been investigated for the first time by SCAPS-1D simulator. The numerical simulation mainly addresses the issues related to amphoteric defects and temperature. Results revealed that theoretically WS₂ can replace the TiO₂ as ETL in PSC, whereby amphoteric defect states have a great influenced on the efficiency of PSC when the density of defect state is higher than 10^{15} cm⁻³. It has also been found that buffer/absorber interface and absorber/HTL interface defects play a major role when the density of defect state is higher than 10¹⁷ cm⁻³ and 10¹⁶ cm⁻³, respectively. Finally, PSC demonstrates better performance when working temperature is in the range of 10 °C-40 °C. The optimized structure with a PCE of 25.70% $(Voc = 1.321 V, Jsc = 25.483 mA/cm^2 and FF = 88.54\%)$ certainly enlightens the possibility of WS₂ as a potential ETL. This investigation opens up the pathway for reasonable usage of any TMDCs, especially WS₂ as the prospective ETL for PSCs.

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Fig. 7. Effect of environmental temperature with respect to (a) Jsc and Voc; (b) efficiency and fill factor of MAPbI₃.

Appendix A. Supplementary data

Supplementary data to this article can be found online at https:// doi.org/10.1016/j.rinp.2018.12.049.

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